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(embedded Dram) and (sdram cc

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Searched Groups for (embedded Dram) and (sdram controller). Results 71 - 80 of about 987. Search took 0.5!

Sorted by relevance [Sort by date](#)Re: Memory specification

... in the Flash form are nearly as dense as **DRAM** and about ... If you don't need speed, **SDRAM**

can be faster than Flash, but you need a more complex **controller**. ...

[comp.arch](#) - Aug 4, 2000 by rickman - [View Thread](#) (11 articles)

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[www.kahlon.com](#)

Interest: [\[link\]](#)Re: [1U] transmeta inside

... Specifications - CPU : VIA Ultra Low Power **Embedded** Eden processor ...
SUSPEND modes,

ACPI & APM - **DRAM** Memory : Onboard ... **SDRAM**, and up to 512MB of
SDRAM on SODIMM ...

[fr.reseaux.internet.hebergement](#) - Nov 6, 2003 by Misterjack - [View Thread](#) (20 articles)

Free Shipping for Memory

Discount Memory Prices
Memory for every computer and MB

[www.memory-man.com](#)

Interest: [\[link\]](#)Re: DRAM for low power application

... built in **DRAM** control that can support **SDRAM**, EDO, self ... My
understanding has always

been that **DRAM** is cheaper ... more power and more complex **controller** (for
refresh ...

[comp.arch.embedded](#) - Jun 21, 2001 by Dave Vanden Bout - [View Thread](#) (4 articles)

Embedded Solutions

MCUs, SBCs, Modules, Displays
Development Kits Ships Same-Day

[www.mouser.com](#)

Interest: [\[link\]](#)RE: SA1110 SDRAM timing calculations

... for SDClk loaded with 50pf, while most **embedded** Systems with ... 221MHz,
however, but

it had no **SDRAM** Interface. ... 0000 1100 0000 0100 0000 04340321 **dram**
refresh reg ...

[mlist.linux.arm](#) - Aug 9, 2001 by Klaus Borchers - [View Thread](#) (12 articles)

High Quality Memory

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[www.4allmemory.com](#)

Interest: [\[link\]](#)Embedded Controllers

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[www.r2-controls.com](#)

Interest: [\[link\]](#)Resume of ASIC/FPGA verification Engineer

... suitable for both real time and PC/AT compatible **embedded** applications. ...
5. Developed

SDRAM buswatcher. ... of all the models on the board like Quicc, **DRAM**, 060
proc ...

[comp.lang.verilog](#) - Jan 28, 2003 by Madhury - [View Thread](#) (2 articles)

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[MemorySuppliers.com](#)

Interest: [\[link\]](#)Проблема выбора микроконтроллера

... почти любой памяти (EDO, **DRAM**, ROM, Flash, AS>> SRAM, SMROM,
SDRAM). ... а и потом берете например **embedded-QT** и ...

[fido7.ru.embedded](#) - May 23, 2002 by Artem Bondarenko - [View Thread](#) (44 articles)

Embedded Controller

Very easy to use, Basic Language,
Low Cost, hundreds of examples.

[www.rfdigital.com](#)

Interest: [\[link\]](#)Celleron socket 370 Van een 500 MHZ .Front side 66Mhz Naar een ...

... Audio Chip (Option for motherboard that **embedded** Audio Chip ... **DRAM**

Embedded DRAM & SRAM

A Technical Analysis of Market,
Technology, Design & Apps Trends

[www.memorystrategies.com](#)

Interest: [\[link\]](#)

Memory Support:Support

3 x 168-pin DIMM ... in either EDO (66MHz only) or **SDRAM**
(66MHz/100MHz ...

[See your message here...](#)

[alt.comp.hardware.overclocking](#) - Oct 6, 2001 by Lviss - [View Thread \(2 articles\)](#)

Re: AT91 + Bluetooth

... I designed the board so that it can take 7212 or 7312, just fit **SDRAM** if it's a
7312 or EDO-**DRAM** if it's a ... Lewin ARW Edwards (**Embedded Engineer**) Personal ...
[comp.arch.embedded](#) - Mar 20, 2001 by Lewin A.R.W. Edwards - [View Thread \(12 articles\)](#)

ZEN News, February 1998

... company's PhantASM single chip graphics accelerator - an **embedded DRAM** product with ... slowing
the transition from the PC100 **SDRAM** to the Rambus Direct **DRAM**. ...
[comp.arch](#) - Mar 2, 1998 by Zahir Parpia - [View Thread \(1 article\)](#)

to anyone who is still unsure....

... GB/sec RAM Micron 64 MB DDR **SDRAM** Memory Bandwidth ... Latency A-Memory 16MB (81MHz **DRAM**)
Disc Drive ... Synthesizer" Clock Frequency 147.456MHz **Embedded** Cache VRAM ...
[microsoft.public.xbox](#) - Oct 21, 2002 by Steedus - [View Thread \(2 articles\)](#)

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